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9 Language portability across shared memory multiprocessors

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Jehn-Ruey Jiang; Shing-Tsaan Huang;

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Tsuyuno, K.; Yamada, S.; Fujii, T.;
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www.sigda.org/Archives/ProceedingArchives/Dac/Dac96/papers/1996/dac96/pdffiles/24_4.pdf - [Similar pages](#)

[PPT]Chapter #12: Controller Implementation Contemporary Logic Design

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... 14 Register Transfer **operations** become 22 Microoperations: ... flexibility – complete parallel access to **datapath** control points. ... Group **mutually exclusive** signals. ...
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[PDF]Chapter #12: Controller Implementation

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... Alternative Ways to Implement **Processor** FSMs • "Random ... ROM, decoded externally some **mutually exclusive** signals can ... 14 Register Transfer **operations** become 22 ...
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[PDF]Multiple Behavior Module Synthesis Based on Selective Groupings

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... The simplicity of the **operations** makes P class instructions easily ... We also assume they are **mutually exclusive** to one another ... 6, ie, a **datapath** for DFG-A and DFG ...
jamaica.ee.pitt.edu/Archives/ProceedingArchives/Date/Date98/papers/1998/date98/pdffiles/05a_3.pdf - [Similar pages](#)

[PDF]FSMD Functional Partitioning for Low Power

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... 2. Here, we have two smaller **mutually-exclusive** processors. ... The **datapath** of **processor** 1 is not consuming power ... types, as well as complex data **operations** in its ...
jamaica.ee.pitt.edu/Archives/ProceedingArchives/Date/Date99/papers/1999/date99/pdffiles/01b_1.pdf - [Similar pages](#)
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Bridge

... Instruction Set **Processor** Specifications (ISPS): The Notation and Its Applications ... Pizarro, Mitch Mlinar, MAHA: a program for **datapath** synthesis, Proceedings ...
www.acm.org/pubs/citations/proceedings/dac/285730/p415-tseng/ - 34k - [Cached](#) - [Similar pages](#)

[PDF]EE182 Computer Organization and Design Winter 1998 Chapter 5 ...

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... Cycle Implementation Slide 1 EE 182 – Winter 1989 EE182 Computer Organization and Design Winter 1998 Chapter 5 Lectures **Processor Datapath** and Control Part II ...
www.cs.huji.ac.il/~comarc/slides/Chapter5_2.pdf - [Similar pages](#)

Chapter Nine

... These **operations** can be further decomposed into 22 ... be ruled out by the **datapath** bussing strategy ... can group the control signals into **mutually exclusive** sets to ...
www.ice.cycu.edu.tw/~vlsi/Chapter9/Chapter9.htm - 31k - [Cached](#) - [Similar pages](#)

Optimization Efficiency in Behavioural Synthesis

... layout of the system, the **datapath** has 54 ... data path units activated by the **operations** in the control steps are shared unless they are **mutually exclusive**. ...
www.ecs.soton.ac.uk/publications/rj/1994/dag/baker/paper3.html - 20k - [Cached](#) - [Similar pages](#)

[PDF]Good morning

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... as well as on **mutually exclusive operations** in different ... multiplexers, control logic, and **datapath** logic including ... pipelines of loops, **operations**, and functions ...
www.edacafe.com/technical/papers_pdf/get2chip1.pdf - [Similar pages](#)

[PPT]Chapter 12 Control Implementation

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... 14 Register Transfer **operations** become 22 Microoperations: ... most flexibility – parallel

access to **datapath** control points. ... Group **mutually exclusive** signals. ...
mobilenet.snu.ac.kr/COURSES/DigitalLogic/ 2003/chap12.ppt - [Similar pages](#)

[PDF]Processor Design MU0 Instruction Set Architecture

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... you) can verify the validity of the **datapath** by testing the ... within the **processor** which are used for internal **operations**. ... AND A and B EOR Bitwise **exclusive-OR** A ...
www.cs.man.ac.uk/teaching/electronics/ CS121/proc_des4.pdf - [Similar pages](#)

[PS]University of Manchester

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... you) can verify the validity of the **datapath** by testing ... As these two operations are **mutually exclusive** it is ... the same wires for the data in both **operations**. ...
www.cs.man.ac.uk/teaching/electronics/ CS121/proc_des4.ps - [Similar pages](#)
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[PS]1

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... 8i1;i2 2 I;(i1;i2) **mutually exclusive**. ... wrote the specification of a 32-bit **datapath**, RISC-style ... load, store, jump, ALU, multiply and variable shift **operations**. ...
www.cag.lcs.mit.edu/~rinard/paper/aspdac01.ps - [Similar pages](#)

[PS]High-level Specification and Efficient Implementation of ...

File Format: Adobe PostScript - [View as Text](#)

... We wrote the specification of a 32-bit **datapath**, RISC-style, linearly pipelined **processor** ... variable shift **operations**. ... 8i1;i2 2 I;(i1;i2) **mutually exclusive**. ...
www.cag.lcs.mit.edu/~cristina/ research/papers/asp-dac01.ps - [Similar pages](#)
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[PDF]Acrobat Distiller, Job 50

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EECS150 Section 10 Controller Implementations Fall 2001 EECS150 - Fall 2001 1-2
Implement of **Processor** FSMs Classical Finite State Machine Design Divide and ...
organics.eecs.berkeley.edu/~viveks/ cs150/lectures/section10.pdf - [Similar pages](#)

[PS]Copyright

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... fine-grainness and flexibility are **mutually exclusive** properties; we ... necessary to span the entire I/O **datapath**). ... Any scalar data **operations** will take multiple ...
www.cse.psu.edu/~ugrain/vlsi-cad/MGAP/frontiers95.ps - [Similar pages](#)

[PDF]Compiler-Directed ILP Extraction for Clustered VLIW/EPIC machines ...

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horizon.ece.utexas.edu/~jacome/nova/ Publications/SatishModulo.pdf - [Similar pages](#)

[PPT]www.cs.plu.edu/courses/csce480/notes/micro_17_cpu3.ppt

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... The **datapath** in a CPU is broken up by ... programming" at the level of register transfer **operations**. ... GOAL: group **mutually exclusive** signals into a binary code and ...
[Similar pages](#)

[PS]INSTRUCTION-LEVEL PARALLELISM IN ASYNCHRONOUS PROCESSOR ...

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... The **operations** of each of the stages are further exposed ... is arbitrated by the CU through **mutually-exclusive** assertions of ... Cycle Time (ICT) **Datapath** Latency (MDL ...
www.dcs.ed.ac.uk/home/dka/belgium94.ps - [Similar pages](#)

[doc]Proceedings Template - WORD

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... developed the architecture view which separates the control from the **datapath**. ... Due to resource sharing, most of these **operations** are **mutually exclusive**. ...
www-cad.eecs.berkeley.edu/~sjweber/DAC_paper_v1.11.doc - [Similar pages](#)

[PDF]Evaluation and Guidance in Processor Architecture Selection for ...

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... Evaluation and Guidance in **Processor** Architecture Selection for DSP 1 Chapter 1 ... signal processing (DSP), the use of mathematical **operations** and algorithms to ...
www-cad.eecs.berkeley.edu/HomePages/naji/research/ phd_dissertation/Dissertation.pdf - [Similar pages](#)

[PDF]An Emulator for Exploring Coarse-Grained Configurable ...

File Format: PDF/Adobe Acrobat - View as HTML

... zero time so that arbitrarily many **operations** can be ... the data stream and sent to the **datapath**. ... read and write address spaces were always **mutually exclusive**. ... www.bergs.com/stefan/papers/fpl01.pdf - [Similar pages](#)

[PDF]Introduction

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... reducing the maximum number of serial **operations** in the ... It implements the signi cand **datapath** in two ... normalizing left shift to be **mutually exclusive** with only ... www.jucs.org/jucs_4_2/the_snap_project_building/paper.pdf - [Similar pages](#)

[PDF]Exploiting Fine-Grain Parallelism of IDEA Using Xilinx FPGA

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... IDEA each round has many **mutually exclusive operations**, which could ... The three **operations** along with the pipeline registers ... Figure 3 shows the **datapath** of our ... webpages.uah.edu/~gurumas/publication1.pdf - [Similar pages](#)

[PDF]Multi-view Operation-level Design – Supporting the Design of ...

File Format: PDF/Adobe Acrobat - View as HTML

... multiple specifications of a single **processor** conform to ... interesting configurations of the **datapath** described in ... To extract the **operations**, we first translate ... https://www.gigascale.org/pubs/384/ERL_tech_report.pdf - [Similar pages](#)

[PPT]Computer Systems Overview

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... No. Structure of a Computer. **Datapath**. ALU Block Diagram. ... Decouple memory system from. internal **processor** operation. ... Diagram. Register transfer **operations**: <Reads>. ... supercom.yonsei.ac.kr/class/DS003-2003/lecture/cs-chap1.ppt - [Similar pages](#)

[PS]Controller Implementation (C) K. Nickels - No. 12-1 Chapter #12: ...

File Format: Adobe PostScript - View as Text

... Reset, Current State, a, b) Next State Register Transfer **Operations**. ... **Datapath** Control. ... signals in ROM, decoded externally some **mutually exclusive** signals can be ... www.engr.trinity.edu/~knickels/class/engr4365/f98/handouts/ch12.ps - [Similar pages](#)

[PS]IEICE TRANS. FUNDAMENTALS, VOL. E00-A, NO. 3 MARCH 1998 1

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... P 1. In addition, either operation 8 or operation 16 also shares the **processor** with the **mutually exclusive** operation 14. ... **datapath** synthesis," in Proc. ... www.elc.ees.saitama-u.ac.jp/~kazuhito/paper/IEICE98.ps - [Similar pages](#)

[PDF]Determining the Order of Processor Transactions in Statically ...

File Format: PDF/Adobe Acrobat - View as HTML

... order for a given **processor** assignment, actor ... all the three scheduling **operations** — assigning actors ... The classic controller/**datapath** architecture is also in ... ptolemy.eecs.berkeley.edu/publications/papers/97/order/order.pdf - [Similar pages](#)

[PDF]Automatic architectural synthesis of VLIW and EPIC processors

File Format: PDF/Adobe Acrobat - View as HTML

... addsubw_2 gpr pr Figure 3. A **datapath** example ... For predi-cated **operations**, a separate predicate input ... within an operation group are **mutually exclusive** while, by ... www.hpl.hp.com/research/itc/carl/papers/Architectural.pdf - [Similar pages](#)

[PDF]ShiftQ: A Buffered Interconnect for Custom Loop Accelerators

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... is generated by customizing control **datapath** and storage ... and at what time these **operations** should occur ... Accelerators Figure illustrates a **processor** that uses a ... www.hpl.hp.com/techreports/2001/HPL-2001-255.pdf - [Similar pages](#)
[More results from www.hpl.hp.com]

Microprogramming History -- Mark Smotherman

... encoding one of several **mutually exclusive** choices (eg ... cycle in which the **datapath** microoperations are ... An alternative approach to multiple **operations** per cycle ... www.cs.clemson.edu/~mark/uprog.html - 50k - [Cached](#) - [Similar pages](#)

[PDF]HDLGen: Architecture Description Language driven HDL Generation ...

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... 6.3 Data Path The implementation of **datapath** consists of two parts. ... using a bus, and load and store **operations** that are **mutually exclusive** with the move. ... www.cecs.uci.edu/technical_report/TR03-04.pdf - [Similar pages](#)

[PDF]Microsoft PowerPoint - Compilers and DSPs.pptFile Format: PDF/Adobe Acrobat - [View as HTML](#)

... scheduling) Phases considered **mutually independent** Each ... How to Exploit Registers in of Pipelined **Datapath**? Chained **operations**: "Pipelined" MAC Many special ...
[lapwww.epfl.ch/courses/conproav/ Compilers%20and%20DSPs.pdf](http://lapwww.epfl.ch/courses/conproav/Compilers%20and%20DSPs.pdf) - [Similar pages](#)

[PDF]G22.3033-004 Architecture and Programming of Parallel Computers ...File Format: PDF/Adobe Acrobat - [View as HTML](#)

... 1): Data Parallel • Programming model – **operations** performed in ... little memory
 – a control **processor** issues instructions ... only 32-bit **datapath** slices would ...
[www.cs.nyu.edu/courses/fall01/G22.3033-004/ lectures/lect2.pdf](http://www.cs.nyu.edu/courses/fall01/G22.3033-004/lectures/lect2.pdf) - [Similar pages](#)

[PS]HDLGen: Architecture Description Language driven HDLFile Format: Adobe PostScript - [View as Text](#)

... The implementation of **datapath** consists of two parts. ... that is implemented using a bus, and load and store **operations** that are **mutually exclusive** with the ...
www.ics.uci.edu/~pmishra/Publications/hdlgenTR.ps - [Similar pages](#)

[doc]\documentstyle[fullpage,12pt]{article}File Format: Microsoft Word 97 - [View as HTML](#)

... in High Performance **Processor Architectures**," (with ... Regularity Extraction in **Datapath Circuits**," (with ... to Determine **Mutually Exclusive Operations** in Behavioral ...
www.ics.uci.edu/~rgupta/Vita2002.doc - [Similar pages](#)
[\[More results from www.ics.uci.edu \]](#)

Details Sen

... state machine design; **datapath** functions, counters ... **Mutually exclusive** with: U2.043
 Computer Science ... Accounting and management; **Operations** management; Marketing ...
[www.eelab.usyd.edu.au/ee_database/ basic/detailsSen.html](http://www.eelab.usyd.edu.au/ee_database/basic/detailsSen.html) - 24k - [Cached](#) - [Similar pages](#)

[PDF]Automated Composition of Hardware ComponentsFile Format: PDF/Adobe Acrobat - [View as HTML](#)

... component communication. The collection of **operations** necessary to perform a data transfer is referred to as a function. 4.1 Component ...
[akebono.stanford.edu/users/nanni/ research/sys/inter_dac98.pdf](http://akebono.stanford.edu/users/nanni/research/sys/inter_dac98.pdf) - [Similar pages](#)

[PDF]CS130 Exam # 3 Review NotesFile Format: PDF/Adobe Acrobat - [View as HTML](#)

... of a "Computer" - **Processor** - Control - **Datapath** (ALU) - Memory ... SUB, DIV, MUL
 - Logical **Operations** : AND, OR ... checkbox is NOT **mutually exclusive** | radio is ...
[www.ece.mtu.edu/faculty/btdavis/ courses/wu_cs130/ex3.review.pdf](http://www.ece.mtu.edu/faculty/btdavis/courses/wu_cs130/ex3.review.pdf) - [Similar pages](#)

[PDF]EECS 416 Study OutlineFile Format: PDF/Adobe Acrobat - [View as HTML](#)

... 1 Meg(w/ 16 bit **datapath**) - Concurrent 64k ... Chip Select Logic - Distinct & **mutually exclusive** select for ... as decoder - Able to define logical **operations** for each ...
[www.ece.mtu.edu/faculty/btdavis/courses/ ut_eecs416/studyoutline.fm.pdf](http://www.ece.mtu.edu/faculty/btdavis/courses/ut_eecs416/studyoutline.fm.pdf) - [Similar pages](#)

[PDF]Design Methodology and Optimization TechniquesFile Format: PDF/Adobe Acrobat - [View as HTML](#)

... quad-rail signals, or other **Mutually Exclusive** Assertion Groups ... Logic, Inc., which began **operations** in Minnesota ... determining the worse-case **datapath** delay and ...
www.ece.umn.edu/~smithsco/dissertation.pdf - [Similar pages](#)

[PS]EE216A Lecture 6 1File Format: Adobe PostScript - [View as Text](#)

... be what you want, especially is cases labels are **mutually exclusive**. ... This is called the **datapath** portion of the chip Where ... Used to represent **operations** on multi ...
[www.janet.ucla.edu/~ingrid/ee216a/ lect06_verilog_modified.ps](http://www.janet.ucla.edu/~ingrid/ee216a/lect06_verilog_modified.ps) - [Similar pages](#)

[PDF]Microsoft PowerPoint - L3File Format: PDF/Adobe Acrobat - [View as HTML](#)

... types of temporal restrictions (not **mutually exclusive**) 3 Maximum ... in a **datapath** 3
 Computation being scheduled – may ... in a process network, or **operations** in a ...
[www.janet.ucla.edu/~mbs/courses/ee298-2/ 1998/lectures/L3_6pp.pdf](http://www.janet.ucla.edu/~mbs/courses/ee298-2/1998/lectures/L3_6pp.pdf) - [Similar pages](#)
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[PPT]CPU structureFile Format: Microsoft Powerpoint 97 - [View as HTML](#)

... idea is to split the **datapath** into stages ... Less flexible because **mutually exclusive**

signals must be identified ... still be done by using several simpler **operations**. ...

jjo.free.net.ph/programming/school/cmcs-132/lect09-cpu_structure_and_function.ppt - [Similar pages](#)

[PDF]Target Architecture Oriented High-Level Synthesis for Multi-FPGA ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... during synthesis (c) Generated partitioned **datapath** cut-line ... the potential sharing of **mutually exclusive** resources ... the time frames, for all **operations** $op \in OP$...

www.fzi.de/sim/publications/2000001-paper.pdf - [Similar pages](#)

[PPT]Chapter #12: Controller Implementation Contemporary Logic Design ...

File Format: Microsoft Powerpoint 97 - [View as HTML](#)

... 14 Register Transfer **operations** become 17 Microoperations: ... complete parallel access to **datapath** control points. ... PC, PC + 1 -> PC, ABUS -> PC **mutually exclusive**. ...

sdgroup.snu.ac.kr/class/digital.2001/note/302L07.ppt - [Similar pages](#)

[PDF]Compilation Increasing the Scheduling Scope for Multi- Memory- ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... herein, designated by region) in the global DFG to generate **datapath** solutions with ... of each multiplier and one clock cycle for the other **operations**, the region ...

w3.ualg.pt/~jmcardo/MyPapers/fpl01.pdf - [Similar pages](#)

[PDF]Petri Nets 2000

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... part of the algorithms • **Datapath operations** may have ... event annotation (from abstract **operations** down to ... and abstract refinement; **processor** example • Low ...

www.daimi.au.dk/pn2000/proceedings/ pn2000_hardwaretut.pdf - [Similar pages](#)

[PPT]specdecode

File Format: Microsoft Powerpoint 97 - [View as HTML](#)

... 64-bit **datapath**. 64-bit data buffer. ... 64KB I-DL1, 512KB unified L2. 2 load / 1 store ports (**mutually exclusive**). ... A store is converted implicitly into 3 **operations**. ...

www.ece.wisc.edu/~pharm/talks/ISCA-29-SPECDECODE.ppt - [Similar pages](#)

[PDF]Computer: PicoRadio Supports Ad Hoc Ultra-Low Power Wireless ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... addressed the partitioning of **operations** between the ... DSPs Dedicated DSP Reconfigurable **datapath** RF filter ... must meet two usually **mutually exclusive** goals: fast ...

bwrc.eecs.berkeley.edu/Background/IEEE%20Computer%20Magazine%20Article/ r7042b.pdf - [Similar pages](#)

[PS]In Proceedings, IEEE/ACM International Conference on Computer- ...

File Format: Adobe PostScript - [View as Text](#)

... RTL Circuit(controller + **datapath**) Test bench with typical input traces. ... AND **operations**, respectively. ... since the decoded state variables are **mutually exclusive**. ...

www.ee.princeton.edu/~anand/pub_links/iccad96_hlest.ps - [Similar pages](#)

Abstracts DATE 98

... The **datapath** word-length has been selected ... algorithm development, new **processor** and memory ... Algorithm to Determine **Mutually Exclusive Operations** in Behavioral ...

ballade.cs.ucla.edu/~kohcc/sigdacdrom/date98/papers/ 1998/date98/htmlfiles/sun_sgi/frames/dateabs.htm - 101k - [Cached](#) - [Similar pages](#)

[PDF]Complex Streamed Media Processor Architecture

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... executed VIS instruc- tions are instructions associated with overhead **operations**, such as ... pressure on the decode/issue logic of a superscalar **processor** and can ...

ce.et.tudelft.nl/publicationfiles/ 682_5_Dmitry_bw_13-02_changed.pdf - [Similar pages](#)

[PDF]Lecture 6: High-Level Logic Design Hardware Description Languages

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... you want, especially is cases labels are **mutually exclusive**. ... Dataflow This is called the **datapath** portion of ... important • Used to represent **operations** on multi ...

www2.ele.ufes.br/~ailson/microeletronica/ stanford_ee271/lect_06.pdf - [Similar pages](#)

[PDF]Design of an Asynchronous PowerPC Microprocessor

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... BO/BI, SH, and CRF channels are **mutually exclusive** with respect ... 4.2 Logical **Operations** Logic is very simple ... requires not just a single **datapath**, but sequencing ...

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[PDF]Asynchronous DRAM Design and Synthesis

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... DRAM) as the basis for a fast cache in the context of an asynchronous **processor**. ... Two transistor(2T) memory cell A DRAM with a one-bit wide **datapath** is formed ...
vlsi.cornell.edu/~rajit/ps/dram.pdf - [Similar pages](#)

[PDF]IXP1200 Network **Processor**

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... vi IXP1200 Network **Processor** Hardware Reference Manual 5.3 ... 6.3.1 Read and Write **Operations**.....6-8 ...
www.hcs.ufl.edu/~dilakano/papers/HardwareRef.pdf - [Similar pages](#)

COMPUTER SYSTEMS

... to the CPU **datapath** and functional ... simultaneously fetch many **mutually exclusive**, simple instructions ... of simultaneous multithreading, **operations** from multiple ...
www.ece.neu.edu/projects/unesco/drafts/6-45-2-8.htm - 101k - [Cached](#) - [Similar pages](#)

United States Patent Application: 0020133784

... groups that are marked as **mutually exclusive** may be ... these design guidelines, the **datapath** synthesizer attempts ... operation groups that are **exclusive**, and places ...
appft1.uspto.gov/.../Schlansker&RS=IN/ Schlansker - 101k - [Cached](#) - [Similar pages](#)

[PS]1

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... the right places, and that no **datapath** or computational ... simultaneously, because they each require **exclusive** access to ... in each subgraph are **mutually compatible**. ...
www.cs.virginia.edu/~vision/papers/cvgip2.ps - [Similar pages](#)

[PDF]Microsoft PowerPoint - L02

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... dispatch times: The maximum time from the **processor** recognizing the ... classification: three types of temporal restrictions (not **mutually exclusive**) Maximum: no ...
nesl.ee.ucla.edu/courses/ee202a/ 2001f/lectures/L02_2pp.pdf - [Similar pages](#)

[PPT]Wireless Node Architectures

File Format: Microsoft Powerpoint 97 - [View as HTML](#)

... three types of temporal restrictions (not **mutually exclusive**). ... Functions or **operations** called actions may be executed ... an internal or external **datapath** or object ...
nesl.ee.ucla.edu/courses/ee202a/2001f/lectures/L02.ppt - [Similar pages](#)
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[PDF]Implementing Optimizations at Decode Time

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... an instruc- tion is decoded into multiple **operations**. ... port, load/store ports are **mutually exclusive** Table 2 ... 404 Cache / Memory 64-bit **datapath** Bypass Network ...
systems.cs.colorado.edu/ISCA2002/FinalPapers/ ikim_isca2002_cameraready.pdf - [Similar pages](#)

Design, Automation and Test in Europe

... **processor** into multiple simpler **mutually-exclusive** communicating processors ... only one smaller **processor** is active ... a complete set of **datapath operations** that can ...
www.date-conference.com/conference/1999/abstracts.htm - 101k - Sep 18, 2003 - [Cached](#) - [Similar pages](#)

[PDF]CC

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... fast bit permutation, n-dimensional matrix masking **operations**. ... some linked to the real **datapath**, all doing ... they have an embedded **processor**, several megabytes ...
www.esd.ecs.soton.ac.uk/research/ phdprojects/phd2002.pdf - [Similar pages](#)

[PS]TO APPEAR IN THE IEEE TRANSACTIONS ON VERY LARGE SCALE ...

File Format: Adobe PostScript - [View as Text](#)

... Bth sample time, exactly enough **operations** would have ... fact, the characteristics are not **mutually exclusive**; it is ... and the DSP comprise the **datapath**, while the ...
www.mtl.mit.edu/research/icsystems/ research/pdf/gutnik_sivlisi97.ps - [Similar pages](#)

[PS]Internal Report Transformational Design of a Direction Detector ...

File Format: Adobe PostScript - [View as Text](#)

... was possible because **mutually exclusive operations** could be ... definition of **OPERATIONS ALLOCATION SCHEDULING**. ... WFJ.Verhaegh, Flexible **Datapath** Compilation for ...
wwwes.cs.utwente.nl/cadtes/archive/ trades/papers/psc.ps - [Similar pages](#)

[PDF]Designing Correct Circuits Specifying and Property Checking the ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... More details of the ARM processor are given in ... load (right hand sides) and store operations (left hand ... into eight classes according to their datapath activities ...
www1.bcs.org.uk/DocsRepository/02300/2334/liu.pdf - [Similar pages](#)

[PS] [ELECTRONIC WORKSHOPS IN COMPUTING Series edited by Professor CJ ...](#)

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... eight classes according to their datapath activities ... Mutually exclusive requests from the execution unit to the ... Exclusive access to the MAR is thus guaranteed at ...
www1.bcs.org.uk/DocsRepository/02300/2334/liu.ps - [Similar pages](#)

[PS] [Hardware Estimation and Validation](#)

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... give us a better estimate of the datapath area ... per- form enclosed operations in a mutually exclusive manner ... Operations in dif- ferent branches of an if statement ...
www.cse.iitd.ernet.in/esproject/homepage/docs/projects/2001-2002/hw_est/thesis.ps - [Similar pages](#)

[PDF] [Register-Transfer Level Estimation Techniques for Switching ...](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... & Power Models RTL Circuit (controller + datapath) Test bench ... Q represent the Boolean OR and AND operations, respectively ... circuit that is a pre-processor for a ...
cse.lab.snu.ac.kr/course/csd99/paper/2_9.pdf - [Similar pages](#)

[PDF] [Enhancing Data Cache Performance via Dynamic Allocation](#)

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... We could have optimized the datapath to sup ... Due to the mutually exclusive relationship between data in L1 ... are fully pipelined, and cache operations are lockup ...
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